

CLAIM AMENDMENTS

Sub F1
1.-44. (Cancelled)

45. (Previously Presented) A method comprising:
providing a light modulator comprising an array of pixel cells and memory buffers, each memory buffer being associated with a different group of two or more of the pixel cells and each memory buffer being located closer to the associated group of pixel cells than another one of the group of pixel cells; and
during a refresh operation, converting the digital indications stored in the memory buffers into analog voltages to update charges intensities on the pixel cells.

46. (Previously Presented) The method of claim 45, wherein the memory buffers are localized to the different groups.

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47. (Currently Amended) The method of claim 45, wherein the memory buffers comprise a static random access memories.

48. (Previously Presented) The method of claim 45, further comprising:
during the refresh operation, reading the digital indications from the memory buffers.

49. (Previously Presented) The method of claim 45, further comprising:
during the refresh operation, latching the digital indications.

50. (Currently Amended) A light modulator comprising:
an array of pixel cells;
memory buffers being spatially distributed among the pixel cells, each memory buffer being associated with a different group of two or more of the pixel cells and storing a digital indications of associated predetermined voltages; and
digital-to-analog converters to convert the digital indications into analog voltages to update charges on the pixel cells during a refresh operation.

51. (Previously Presented) The light modulator of claim 50, wherein the refresh operation occurs at a different rate than a frame update operation to the pixel cells.

52. (Previously Presented) The light modulator of claim 50, wherein at least one of the memory buffers comprise static random access memory.

53. (New) The method of claim 45, wherein each of the pixel cells is controlled independently with respect to the other pixel cells.

54. (New) The light modulator of claim 50, wherein each of the pixel cells is controlled independently with respect to the other pixel cells.